## Impulse response

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So far circuits have been driven by a DC source, an AC source and an exponential source. If we can find the current of a circuit generated by a Dirac delta function or impulse voltage source  $\delta$ , then the convolution integral can be used to find the current to any given voltage source!

## **Example Impulse Response**

The current is found by taking the derivative of the current found due to a DC voltage source! Say the goal is to find the  $\delta$  current of a series LR circuit ... so that in the future the convolution integral can be used to find the current given any arbitrary source.

Choose a DC source of 1 volt (the real Vs then can scale off this). The particular homogeneous solution (steady state) is 0. The homogeneous solution to the non-homogeneous equation has the form:

Assume the current initially in the inductor is zero. The initial voltage is going to be 1 and is going to be across the inductor (since no current is flowing):

$$v(t)=Lrac{di(t)}{dt}$$
 :  $v(0)=1=L*(-rac{AR}{L})$  :

If the current in the inductor is initially zero, then:

Which implies that:

So the response to a DC voltage source turning on at t=0 to one volt (called the unit response  $\mu)$  is:

 $i_\mu(t)=rac{1}{R}(1-e^{-rac{t}{R}})$ 

Taking the derivative of this, get the impulse ( $\delta$ ) current is:

Now the current due to any arbitrary  $V_{S}(t)$  can be found using the convolution integral:

Don't think  $i_{\delta}$  as current. It is really  $\frac{d}{dt} \frac{current}{1volt}$ .  $V_{S}(\tau)$  turns into a multiplier.

## LRC Example

Find the time domain expression for  $i_0$  given that  $I_s = cos(t + \pi/2)\mu(t)$  amp.

Earlier the step response for this problem was found:

The impulse response is going to be the derivative of this:

$$i_o(t) = \int_0^t i_{o_\delta}(t- au) I_s( au) d au + C_1$$

The Mupad code to solve the integral (substituting x for  $\tau$ ) is:

 $f := \exp(-(t-x)) *\sin(t-x) *(1 + \cos(x)); <br>S := int(f,x = 0..t)$ 

:

## Finding the integration constant

This implies:

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